

PATENT NUMBER

U.S. UTILITY Patent Application

KIV SCANNED	O.I.P.E. QA	PATENT DATE Bonura
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APPLICATION NO 09/583057	CONT/PRIOR	CLASS 21914	SUBCLASS 30	ART UNIT 2763 2184	EXAMINER Bonura
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APPLICANTS

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TITLE

Control method and apparatus for testing of multiple processor integrated circuits and other digital systems

PTO-2040
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner) _____ (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER	
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